

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2091	(257/784).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/05 14:17
L2	1926	(257/786).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/05 14:22
L3	1360	(257/780).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/05 14:29
L4	1778	(257/690).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/05 14:37
L5	2	(active adj1 circuit) and transistor and (buses) and (contact adj1 pad\$1) and (stress adj1 absorb\$3 adj1 layer) and opening\$1 and (contact adj1 pad\$1)	US-PGPUB	OR	OFF	2005/12/05 14:39